

Fig. 1
(Prior Art)

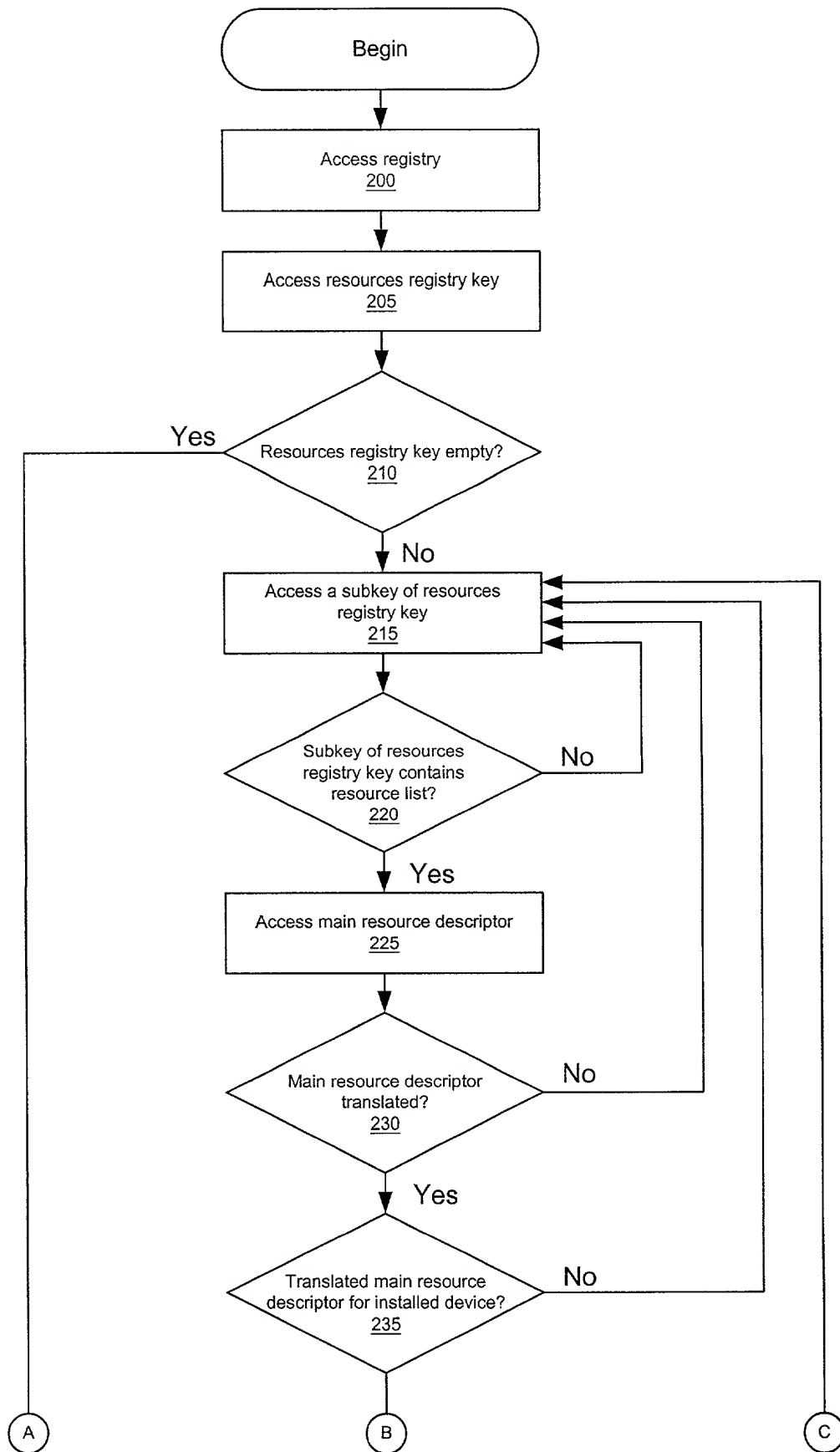


Fig. 2

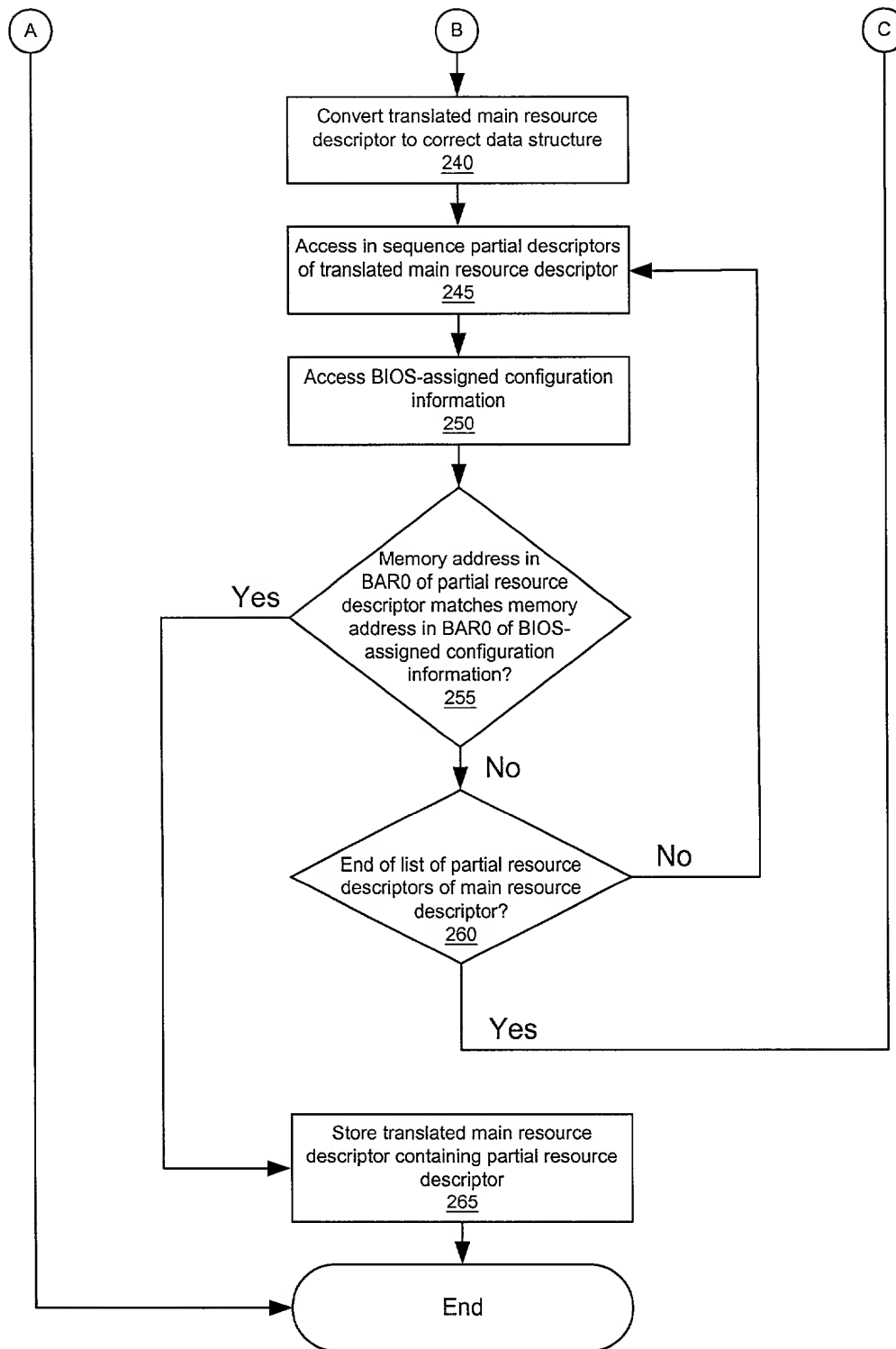


Fig. 2
(continued)

FIG. 3 is a block diagram of a computer system 100, showing the internal components and their interconnections. The system includes a Main Memory 130, Data Storage Device 150, Plug-and-Play Device 300, and a PCI Bus 310. The Main Memory 130 contains an Operating System 131, which includes a Registry 132, Resources Registry Key 133, Resource List 134, Main Resource Descriptor 135, and Partial Resource Descriptors 136. The Data Storage Device 150 includes a Device Driver 151 and a Memory Address Collector 153. The Plug-and-Play Device 300 includes a PCI Configuration Space 301, which contains BIOS-Assigned PCI Configuration Information 302 and a Memory Address in Base Address Register 0 303. The PCI Bus 310 connects the Main Memory 130, Data Storage Device 150, Plug-and-Play Device 300, and the Processor 120. The Processor 120 is connected to the PCI Bus 310 and the ROM 140. The ROM 140 is connected to the PCI Bus 310 and the Flat-Panel Display Device 160. The Flat-Panel Display Device 160 is connected to the PCI Bus 310 and the Network Interface 180. The Network Interface 180 is connected to the PCI Bus 310 and the Alphabetic Input Device 170. The Alphabetic Input Device 170 is connected to the PCI Bus 310 and the Computer System 100.

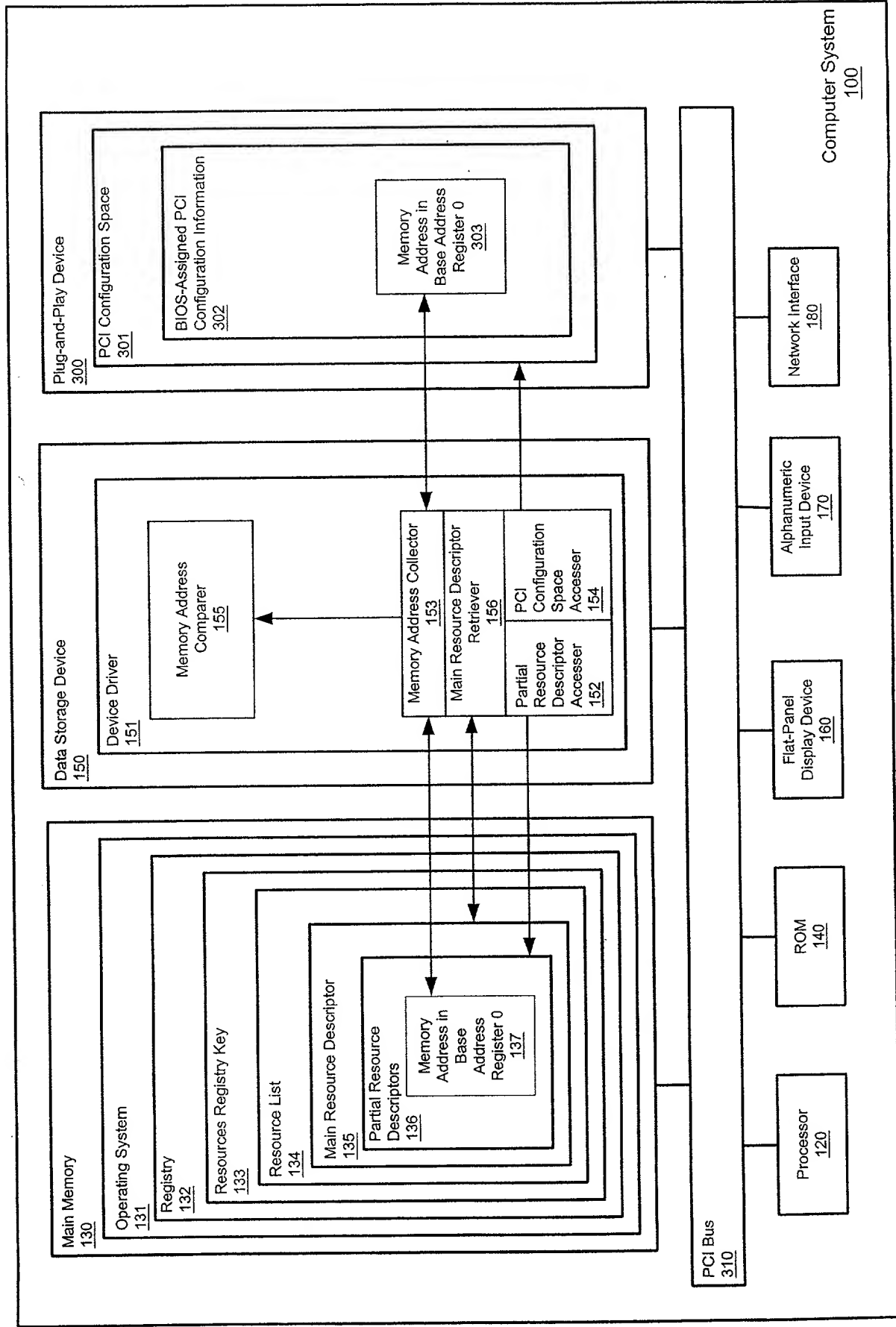


Fig. 3